Revision History

Date	History
11/3/2014	First Released
11/14/2014	Update Pin Map
12/10/2014	Pin Name change

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Size	Document Name	Rev
Custom	Revision History	A

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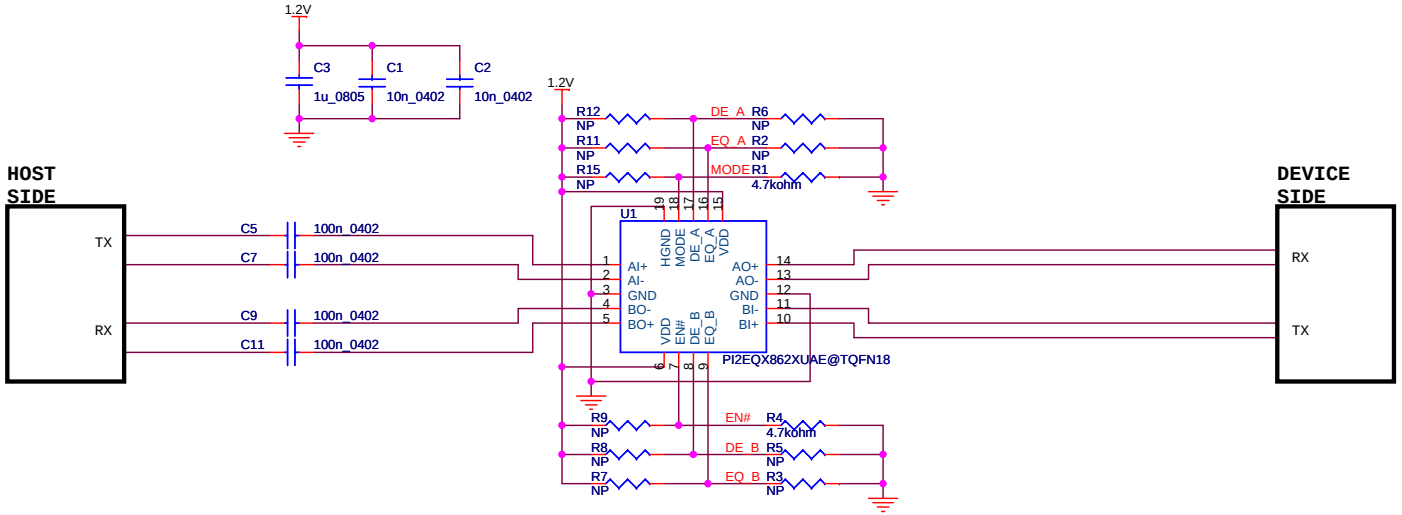
PI2EQX862XUAE Typical SATA/PCIE Application Circuit



PIN CONFIGURATION for CONTROL

PIN NAME	PIN Control FUNCTION
P16:EQ_A P9:EQ_B	Input Equalization for Channel A/B Tri-level control GND: 7dB Open: 4dB VDD: 10dB
P17:DE_A P8:DE_B	PCIE use only, Input Equalization for Channel A/B Tri-level control GND: -2.5dB Open: 0dB VDD: -4dB
P7:EN# P8:MODE	Hi-Z input impedance EN# = Low, MODE = Low: SATA Application EN# = Low, MODE = High: PCIE Application EN# = High, MODE = x: Chip Disable

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B	PI2EQX862XUAE Typical SATA/PCIE Application Circuit	A

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