



PIN CONFIGURATION TABLE

PIN NAME	PIN FUNCTION DESCRIPTION	CONFIGURATION for PI3EQX6741STZDE	CONFIGURATION for PI3EQX6741STBZDE
EN	With Internal 200k-ohm pull-up resistor Low: Power down mode High: Normal Operation	Pin7: EN JP10=Open, Normal Operation JP10=Short, Power down Mode	Pin7: EN JP10=Open, Normal Operation JP10=Short, Power down Mode
A_EQ B_EQ	Tri-level input with internal 100kohm pull-up resistor and 100kohm pull-down resistor	Pin17: A_EQ Equalization is controlled by Pin19: B_EQ JP6 for Channel A, JP8 for Channel B.	Pin17: A_EQ Equalization is controlled by JP6 for Channel A, No EQ for Channel B.
A_EM B_EM	Output Emphasis/Swing Adjustment it is digital control with internal 200kohm pull-up resistor	Pin8: B_EM Emphasis/Swing is controlled by Pin9: A_EM JP3 for Channel A, JP4 for Channel B.	Pin8: B_EM Emphasis/Swing is controlled by Pin9: A_EM JP3 for Channel A, JP4 for Channel B.
TDet_EN	With Internal 200k-ohm pull-up resistor Low: Disable termination detect function High: Enable termination detect function	Pin18:TDet_EN JP7=Open, Enable termination detect function JP7=Short, Disable termination detect function R15 can be removed	Pin19:TDet_EN JP8=NP, Enable termination detect function JP8=GND, Disable termination detect function
TDet_A# TDet_B#	Termination Detect Output, Active High, Open Drain. Low: HDD Termination Present High: HDD Termination NOT Present Only if TDet_EN is high, the chip has this function	Pin13:TDet_A# JP13 for TDet_A# function test Pin3: TDet_B# JP14 for TDet_B# function test	Pin18:TDet_A# JP7 for TDet_A# function test JP13&JP14, R13&R14 should be removed R15 should be added for test.